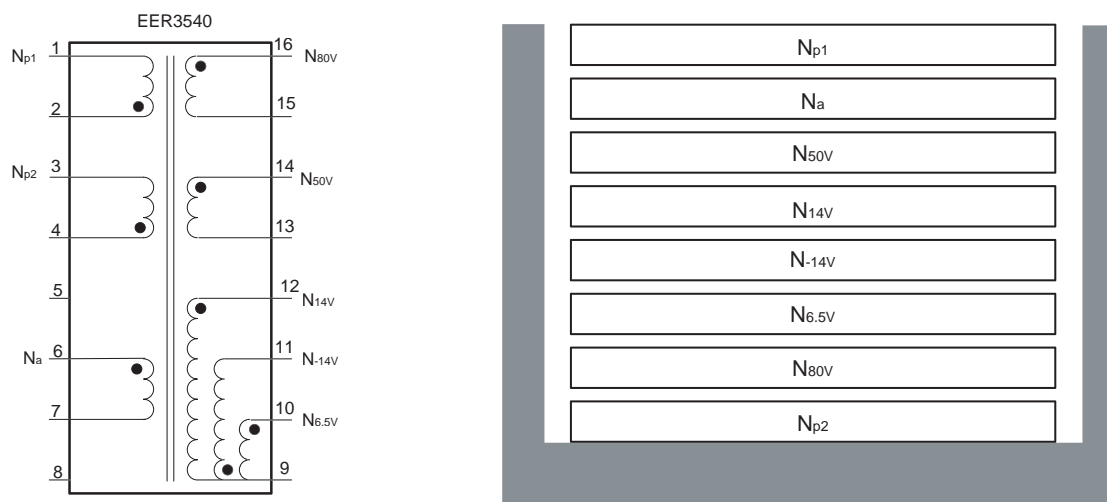


2. Transformer

2.1. Transformer Schematic Diagram



2.2. Winding Specification

	Pin (S → F)	Wire	Turns	Winding Method
N_{p2}	2 → 1	0.3Φ x 2	20	Solenoid winding
Insulation: Polyester Tape t = 0.050mm, 2 Layers				
N_{80V}	16 → 15	0.3Φ x 1	10	Center winding
Insulation: Polyester Tape t = 0.050mm, 2 Layers				
$N_{6.5V}$	10 → 9	0.3Φ x 2	3	Solenoid winding
Insulation: Polyester Tape t = 0.050mm, 2 Layers				
N_{-14V}	9 → 11	0.3Φ x 1	5	Solenoid winding
Insulation: Polyester Tape t = 0.050mm, 2 Layers				
N_{14V}	12 → 9	0.3Φ x 2	6	Solenoid winding
Insulation: Polyester Tape t = 0.050mm, 2 Layers				
N_{50V}	14 → 13	0.3Φ x 3	22	Center winding
Insulation: Polyester Tape t = 0.050mm, 2 Layers				
N_a	6 → 8	0.2Φ x 1	12	Solenoid winding
Insulation: Polyester Tape t = 0.050mm, 2 Layers				
N_{p1}	4 → 3	0.3Φ x 2	20	Solenoid winding
Outer Insulation: Polyester Tape t = 0.050mm, 2 Layers				

Core: EER3540 ($A_e = 107\text{mm}^2$)

Bobbin: EER3540

2.3. Electrical Characteristics

	Pin	Spec.	Remark
Inductance	1-4	420 μH	300kHz, 1V
Leakage	1-4	5 μH	Short all other pins

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

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