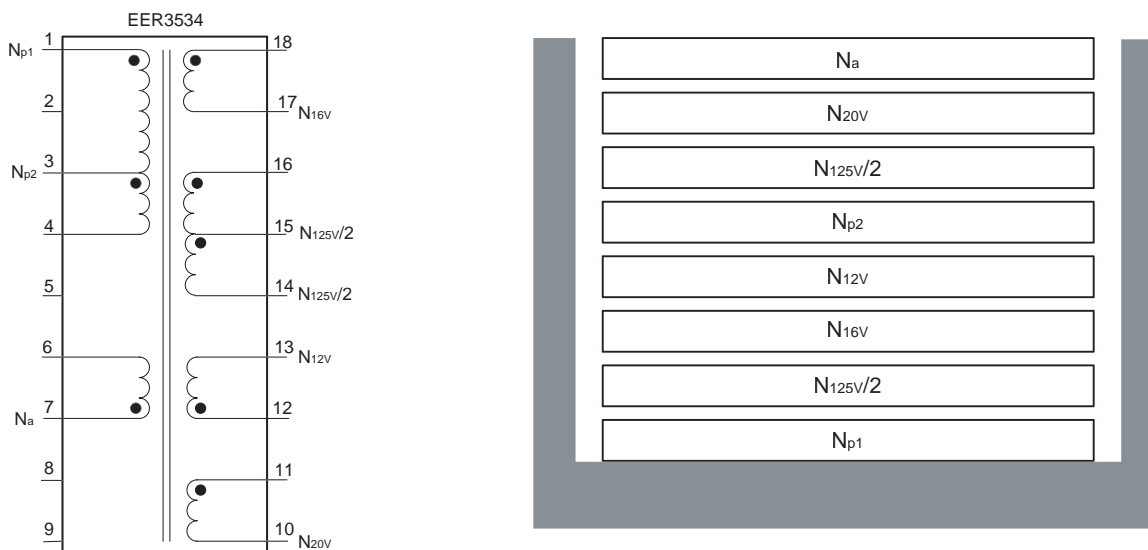


2. Transformer

2.1. Transformer Schematic Diagram



2.2. Winding Specification

	Pin (S → F)	Wire	Turns	Winding Method
N_{p1}	1 → 3	0.6Φ x 1	35	Solenoid winding
$N_{125V/2}$	16 → 15	0.6Φ x 1	28	Solenoid winding
N_{16V}	18 → 17	0.4Φ x 2	8	Solenoid winding
N_{12V}	12 → 13	0.5Φ x 1	6	Solenoid winding
N_{p2}	3 → 4	0.6Φ x 1	35	Solenoid winding
$N_{125V/2}$	15 → 14	0.5Φ x 1	28	Solenoid winding
N_{20V}	11 → 10	0.5Φ x 1	10	Solenoid winding
N_a	7 → 6	0.3Φ x 1	11	Solenoid winding

Core: EER3534 ($A_e = 111 \text{ mm}^2$)

Bobbin: EER3534

2.3. Electrical Characteristics

	Pin	Spec.	Remark
Inductance	1-4	805 μH	
Leakage	1-4		Short all other pins

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PRODUCT STATUS DEFINITIONS

Definition of Terms

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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Rev. 115