

FDD8453LZ_F085

N-Channel Power Trench® MOSFET

40V, 50A, 6.5mΩ

Features

- Typ $r_{DS(on)}$ = 5mΩ at V_{GS} = 10V, I_D = 15A
- Typ $r_{DS(on)}$ = 6mΩ at V_{GS} = 4.5V, I_D = 13A
- HBM ESD protection level > 7kv typical
- RoHS Compliant
- Qualified to AEC Q101

General Description

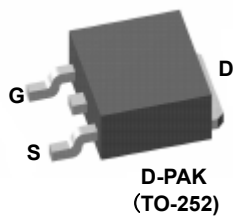
This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been especially tailored to minimize the on-state resistance and switching loss. G-S zener has been added to enhance ESD voltage level.

Applications

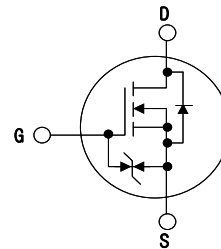
- Inverter
- Synchronous Rectifier



Package



Symbol



MOSFET Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain to Source Voltage	40	V
V_{GS}	Gate to Source Voltage	± 20	V
I_D	Drain Current - Continuous (Package limited) $T_C = 25^\circ\text{C}$	50	A
	- Pulsed	Figure 4	
E_{AS}	Single Pulse Avalanche Energy (Note 1)	88	mJ
P_D	Power Dissipation	118	W
	Dereate above 25°C	0.79	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature	-55 to + 175	$^\circ\text{C}$

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction to Case	1.27	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient, 1in ² copper pad area	52	$^\circ\text{C/W}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD8453LZ	FDD8453LZ_F085	D-PAK(TO-252)	13"	12mm	2500 units

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
--------	-----------	-----------------	-----	-----	-----	-------

Off Characteristics

B_{VDSS}	Drain to Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}, T_C = 150^\circ\text{C}$	-	-	1	μA
			-	-	250	
I_{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	1.0	1.8	3.0	V
$r_{DS(on)}$	Drain to Source On Resistance	$I_D = 15\text{A}, V_{GS} = 10\text{V}$	-	5.0	6.5	$\text{m}\Omega$
		$I_D = 13\text{A}, V_{GS} = 4.5\text{V}$	-	6.0	7.8	$\text{m}\Omega$
		$I_D = 15\text{A}, V_{GS} = 10\text{V}, T_J = 175^\circ\text{C}$	-	9.4	12.2	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS} = 5\text{V}, I_D = 15\text{A}$	-	91	-	S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$	-	2935	-	pF	
C_{oss}	Output Capacitance		-	340	-	pF	
C_{rss}	Reverse Transfer Capacitance		-	260	-	pF	
R_g	Gate Resistance	$f = 1\text{MHz}$	-	1.8	-	Ω	
$Q_{g(ToT)}$	Total Gate Charge at 10V	$V_{GS} = 0$ to 10V	$V_{DD} = 20\text{V}$ $I_D = 15\text{A}$ $I_g = 1\text{mA}$	-	60	78	nC
$Q_{g(5)}$	Total Gate Charge at 5V	$V_{GS} = 0$ to 5V		-	32	42	nC
Q_{gs}	Gate to Source Gate Charge			-	7.5	-	nC
Q_{gd}	Gate to Drain "Miller" Charge			-	13	-	nC

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
--------	-----------	-----------------	-----	-----	-----	-------

Switching Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
t_{on}	Turn-On Time	$V_{DD} = 20\text{V}, I_D = 15\text{A},$ $V_{GS} = 10\text{V}, R_{GEN} = 6\Omega$	-	-	34	ns
$t_{d(on)}$	Turn-On Delay Time		-	12	-	ns
t_r	Rise Time		-	10	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	43	-	ns
t_f	Fall Time		-	7	-	ns
t_{off}	Turn-Off Time		-	-	80	ns

Drain-Source Diode Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
V_{SD}	Source to Drain Diode Voltage	$I_{SD} = 2\text{A}$	-	0.7	1.2	V
		$I_{SD} = 15\text{A}$	-	0.8	1.3	V
t_{rr}	Reverse Recovery Time	$I_F = 15\text{A}, dI_{SD}/dt = 100\text{A}/\mu\text{s}$	-	25	33	ns
Q_{rr}	Reverse Recovery Charge		-	14	19	nC

Notes:

1: Starting $T_J = 25^\circ\text{C}$, $L = 0.11\text{mH}$, $I_{AS} = 40\text{A}$, $V_{DD} = 36\text{V}$ during inductor charging and $V_{DD} = 0\text{V}$ during the time in Avalanche.

This product has been designed to meet the extreme test conditions and environment demanded by the automotive industry. For a copy of the requirements, see AEC Q101 at: <http://www.aecouncil.com/>
 All Fairchild Semiconductor products are manufactured, assembled and tested under ISO9000 and QS9000 quality systems certification.

Typical Characteristics

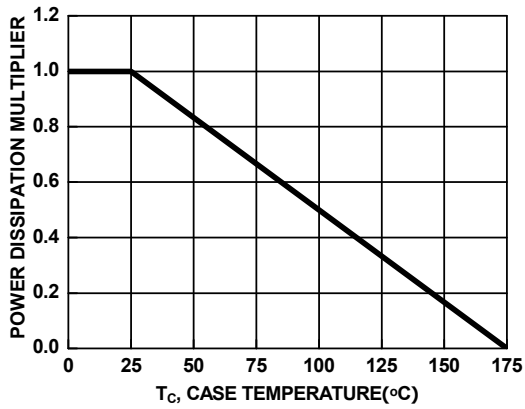


Figure 1. Normalized Power Dissipation vs Case Temperature

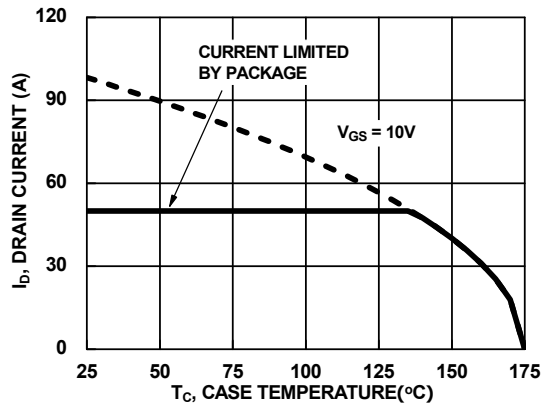


Figure 2. Maximum Continuous Drain Current vs Case Temperature

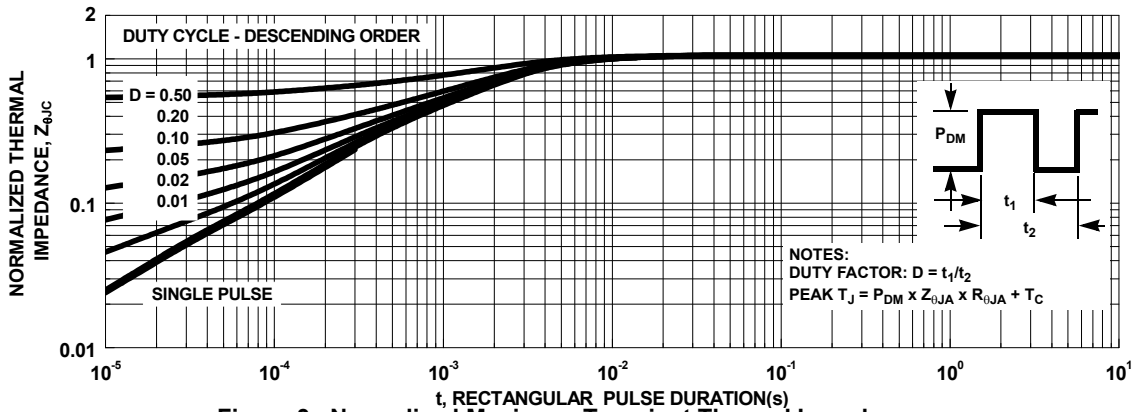


Figure 3. Normalized Maximum Transient Thermal Impedance

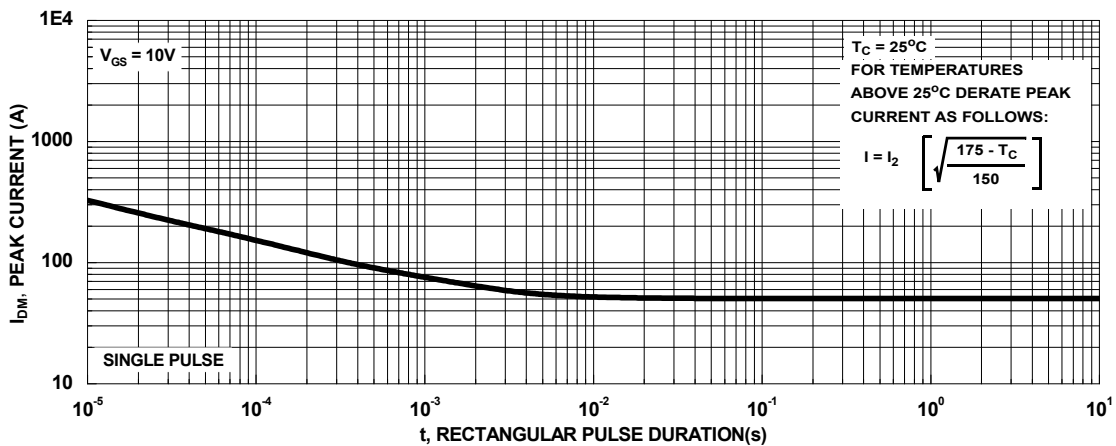


Figure 4. Peak Current Capability

Typical Characteristics

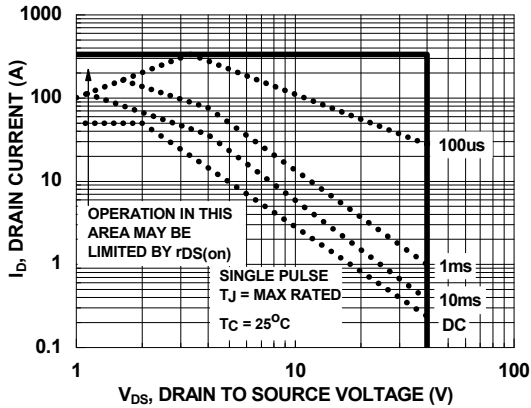
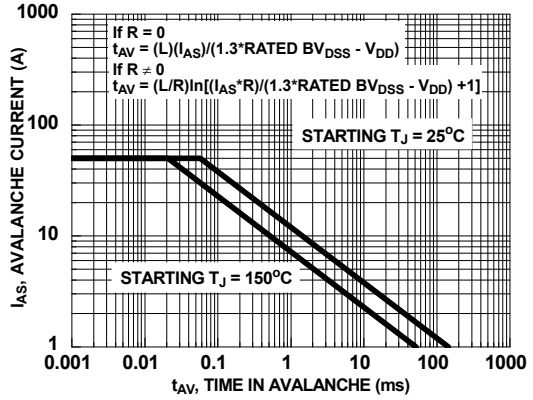


Figure 5. Forward Bias Safe Operating Area



NOTE: Refer to Fairchild Application Notes AN7514 and AN7515
 Figure 6. Unclamped Inductive Switching Capability

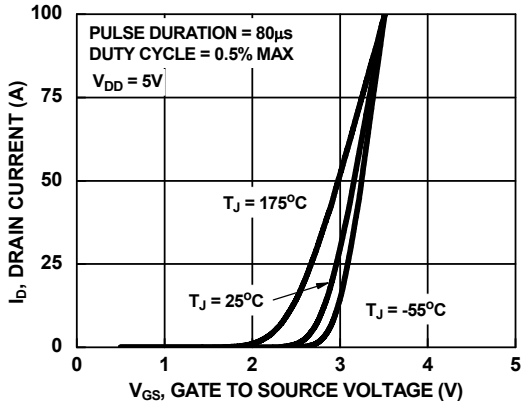


Figure 7. Transfer Characteristics

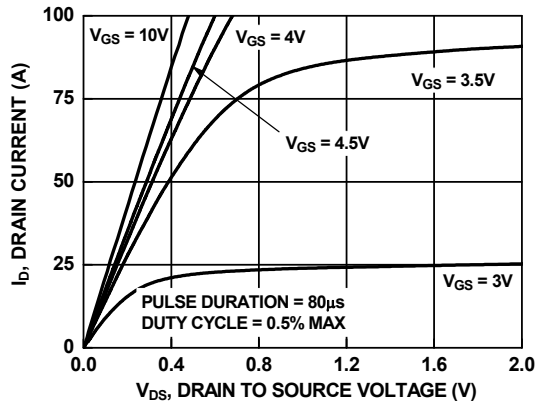


Figure 8. Saturation Characteristics

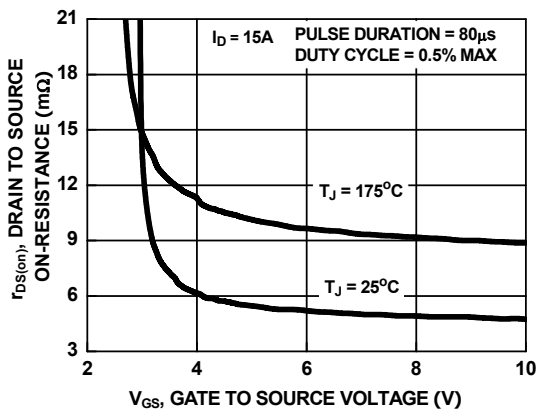


Figure 9. Drain to Source On-Resistance Variation vs Gate to Source Voltage

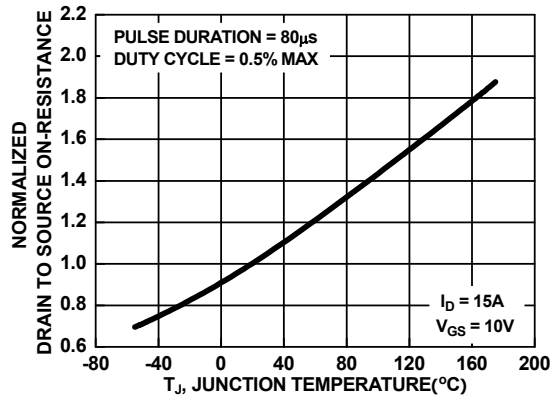


Figure 10. Normalized Drain to Source On-Resistance vs Junction Temperature

Typical Characteristics

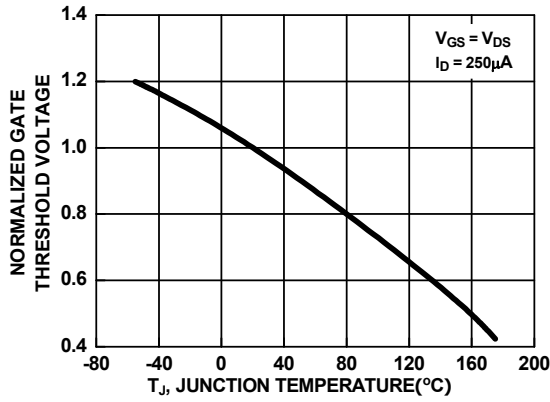


Figure 11. Normalized Gate Threshold Voltage vs Junction Temperature

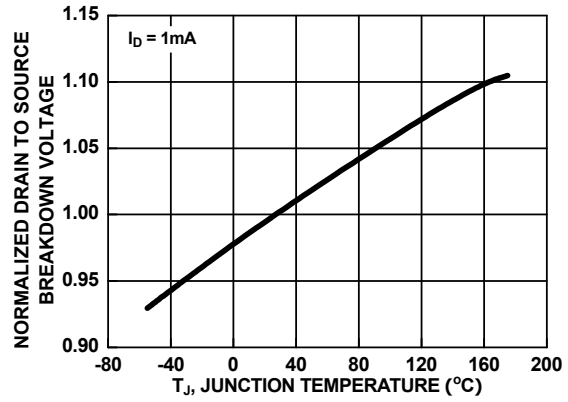


Figure 12. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

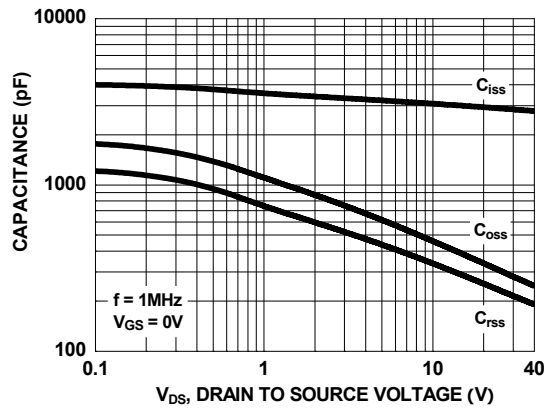


Figure 13. Capacitance vs Drain to Source Voltage

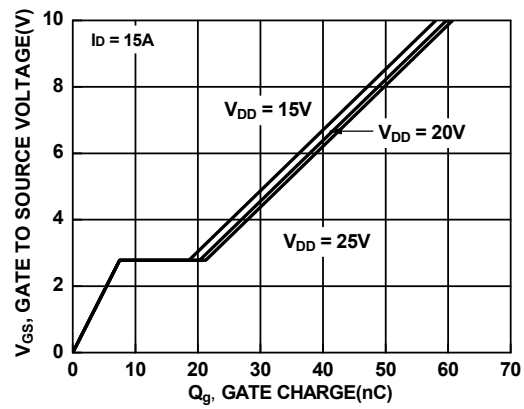






Figure 14. Gate Charge vs Gate to Source Voltage



TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

- | | | | |
|---|-------------------------------------|---|---|
| 2Cool™ | FlashWriter® * | PDP SPM™ | The Power Franchise® |
| AccuPower™ | FPS™ | Power-SPM™ | The Right Technology for Your Success™ |
| Auto-SPM™ | F-PFS™ | PowerTrench® | the |
| AX-CAP™* | FRFET® | PowerXS™ | power |
| BitSiC® | Global Power Resource SM | Programmable Active Droop™ | franchise |
| Build it Now™ | Green FPS™ | QFET® | TinyBoost™ |
| CorePLUS™ | Green FPS™ e-Series™ | QST™ | TinyBuck™ |
| CorePOWER™ | Gmax™ | Quiet Series™ | TinyCalc™ |
| CROSSVOLT™ | GTO™ | RapidConfigure™ | TinyLogic® |
| CTL™ | IntelliMAX™ |  | TINYOPTO™ |
| Current Transfer Logic™ | ISOPLANAR™ | Saving our world, 1mW/W/kW at a time™ | TinyPower™ |
| DEUXPEED® | MegaBuck™ | SignalWise™ | TinyPWM™ |
| Dual Cool™ | MICROCOUPLER™ | SmartMax™ | TinyWire™ |
| EcoSPARK® | MicroFET™ | SMART START™ | TranSiC® |
| EfficientMax™ | MicroPak™ | SPM® | TriFault Detect™ |
| ESBC™ | MillerDrive™ | STEALTH™ | TRUECURRENT®* |
|  | MotionMax™ | SuperFET® | μSerDes™ |
| Fairchild® | Motion-SPM™ | SuperSOT™_3 |  |
| Fairchild Semiconductor® | mWSaver™ | SuperSOT™_6 | UHC® |
| FACT Quiet Series™ | OptiHiT™ | SuperSOT™_8 | Ultra FRFET™ |
| FACT® | OPTOLOGIC® | SupreMOS® | UniFET™ |
| FAST® | OPTOPLANAR® | SyncFET™ | VCX™ |
| FastvCore™ | | Sync-Lock™ | VisualMax™ |
| FETBench™ | |  | XS™ |

*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used here in:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support. Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.