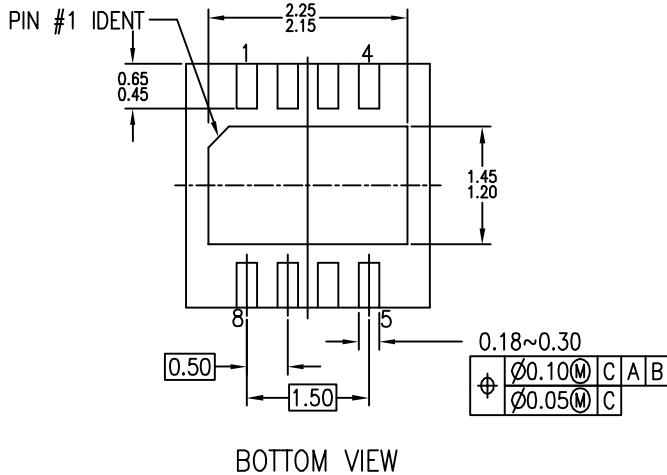
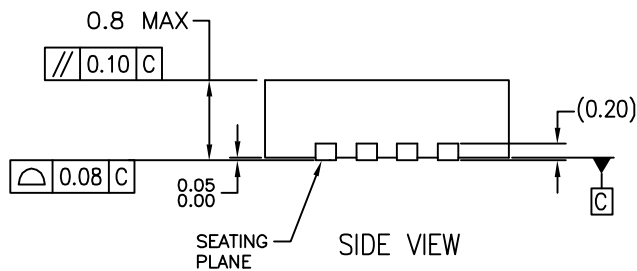
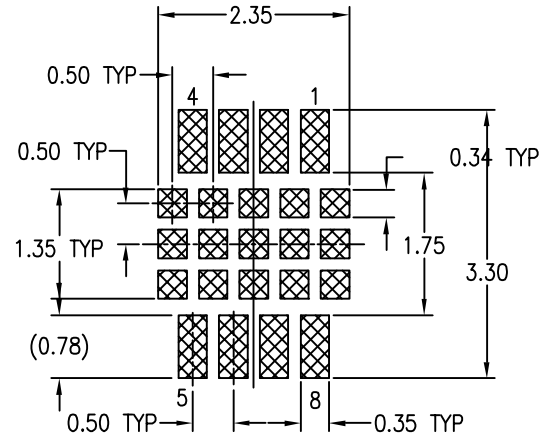
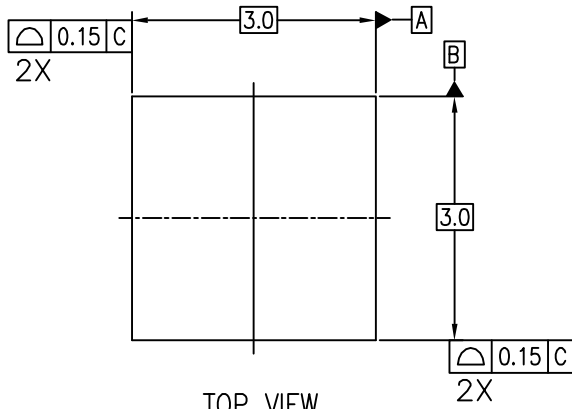


REVISIONS

LTR	DESCRIPTION	EDCN	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL		18-6-2002	FEITAN
2	PAD SIZE REVISED, DRAWING UPDATED TO MEET FSC-MKT-0001		5 JULY 07	L.HUEBENER



NOTES:

- A. CONFORMS TO JEDEC REGISTRATION MO-229, VARIATION VEED-1, DATED 11/2001
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994
- D. DRAWING FILE NAME: MLP08BREV2

APPROVALS	DATE	FAIRCHILD SEMICONDUCTOR		
DRAWN L.HUEBENER	5 JULY 07	Bayan Lepas, FIZ, 11900, Penang, Malaysia.		
DFTG. CHK.		8LD,MLP DUAL,JEDEC MO-229, 3MM SQUARE		
ENGR. CHK.				
		SCALE N/A	SIZE N/A	DRAWING NUMBER MKT-MLP08B
		DO NOT SCALE DRAWING		REV 2
		SHEET 1 of 1		