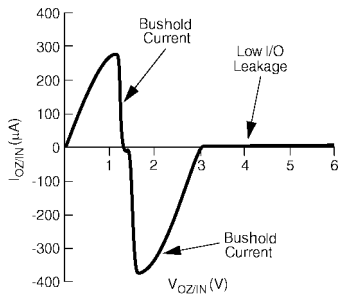


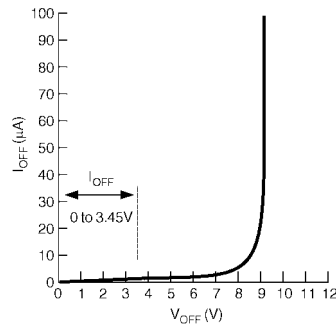
Extended Characterization Data GTLP18T612

DC Characteristics

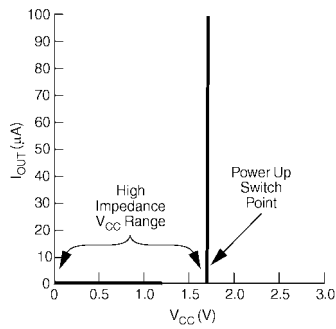
TTL Port – I_{OZ}/I_{IN} Curve
GTLP18T612



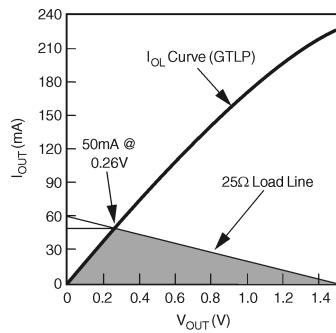
TTL Port – I_{OFF} Curve
GTLP18T612



TTL Port – I_{PU} Curve
GTLP18T612

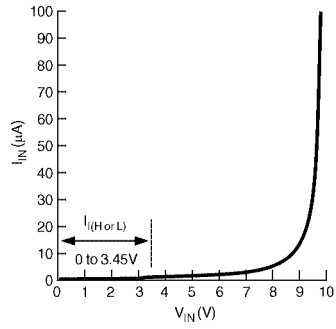


GTLP Port – I_{OL} Curve
GTLP18T612

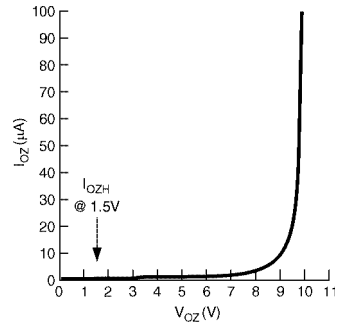


DC Characteristics (Continued)

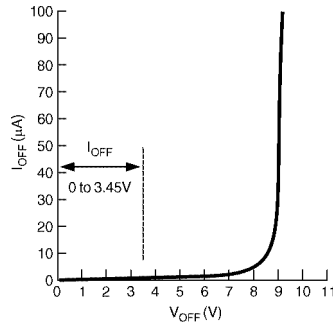
GTLP Port – I_{IN} Curve
GTLP18T612



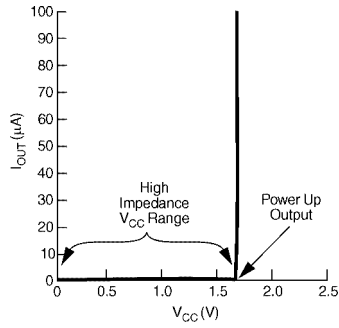
GTLP Port – I_{OZ} Curve
GTLP18T612



GTLP Port – I_{OFF} Curve
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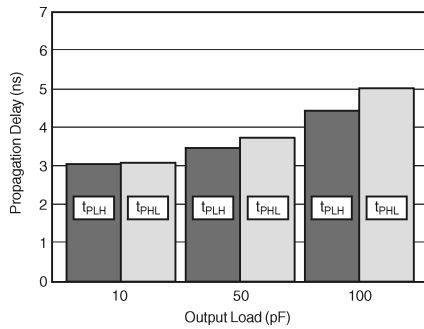


GTLP Port – I_{PU} Curve
GTLP18T612

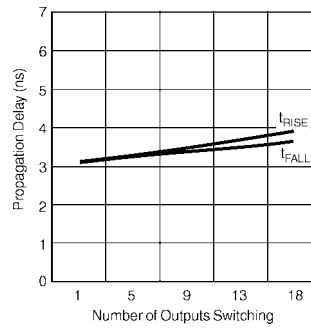


AC Characteristics

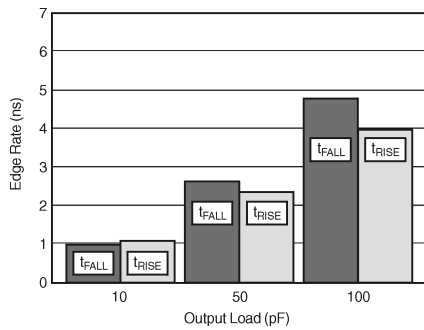
GTLP to TTL Propagation Delay vs. Load
GTLP18T612



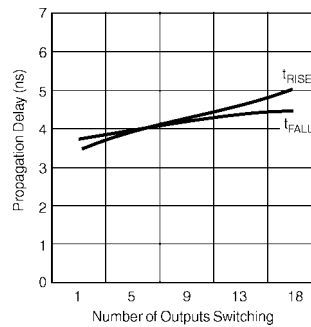
GTLP to TTL Propagation Delay vs. number of outputs switching (10 pF)
GTLP18T612



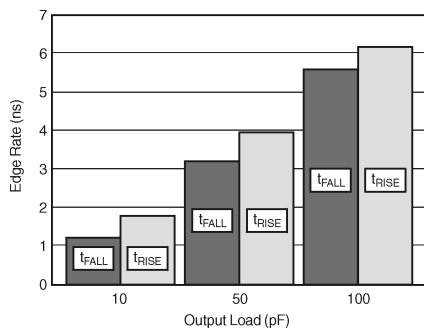
GTLP to TTL Output Edge Rates vs. Load (1 Bit)
GTLP18T612



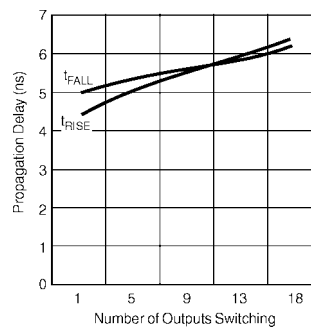
GTLP to TTL Propagation Delay vs. number of outputs switching (50pF)
GTLP18T612



GTLP to TTL Output Edge Rates vs. Load (18 Bit)
GTLP18T612



GTLP to TTL Propagation Delay vs. number of outputs switching (100 pF)
GTLP18T612



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