



Date Created: 1/13/2004  
Date Issued: 1/25/2004  
PCN # 20040208

DESIGN/PROCESS CHANGE NOTIFICATION -- FINAL

This is to inform you that a design and/or process change will be made to the following product(s). This notification is for your information and concurrence.

If you require data or samples to qualify this change, please contact **Fairchild Semiconductor within 30 days of receipt of this notification.**

If you have any questions concerning this change, please contact:

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PCN Type: Marking Format or Content

Effectivity

Expected 1st Device Shipment Date: 4/26/2004  
Earliest Year/Work Week of Changed Product: 0418  
(Note: Package marking may differ from this format)

Product ID (Description):

The proposed change will affect all DMOS and Bipolar devices in SOIC-16L package.

Description of Change:

Change from ink marking to laser marking process. This is part of Fairchild Semiconductor's effort to improve quality, yield, productivity, and cycle time to our SOIC-16L package and improve customer service.

These products are currently assembled, test and finished in Amkor Technology Philippines.

Effect of Change:

There will be no change on product reliability or package dimensions. Current SMT packages assembled in our assembly facility in Cebu and or subcontractor's which includes SOT-23, SOIC-8L and TSSOP-8L are all Laser marked. The units have a constant marking depth of 1 mil in which marking permanency is not an issue.

Qualification:

Qual/REL Plan Numbers

Additional Qualification Data

**Qualification is NOT required for this type of change.**

Affected FSIDs

19S2_B19232A	19S3_B19231A	19S3_B19232A
23S3_B23292A	23S3_B23293A	63S3_B63811A
63S3_B63812A	66S3_B66304A	66S3_B66305A
FDQ3010N	FTM3725	MMPQ2222
MMPQ2222A	MMPQ2369	MMPQ2907
MMPQ2907A	MMPQ3467	MMPQ3724
MMPQ3724A	MMPQ3725	MMPQ3725A
MMPQ3904	MMPQ3906	MMPQ6502
MMPQ6700	NDM3000	NDM3001
SO14ENG	SO16ENG	Y1S3_BY1001A
Y1S3_BY1002A	Y2S3_BY2001A	Y2S3_BY2002A
YAS2_BYA001A	YIS3_BY1001A	