

Fairchild Semiconductor Product Package Material Disclosure

Package Type	SSOT-6 FLMP					
Weight of Package (grams)	Maximum	1.60E-02				
	Minimum	1.50E-02				
Component	Material	Weight in grams	Substance in material	Wt% in finished product min	Wt% in finished product max	CAS #
Lead Frame	Copper alloy	6.34E-03	Copper	39.68	42.13	
			Iron	38.65	41.04	7440-50-8
			Zinc	0.95	1.01	7439-89-6
			Phosphorus	0.05	0.05	7440-66-6
			Lead	0.01	0.01	7723-14-0
				0.01	0.01	7439-92-1
Encapsulation	Epoxy	7.29E-03	Silica	45.62	48.44	
			Carbon Black	28.22	37.63	
			Resin	0.00	0.71	
			Antimony Compound	7.05	16.23	1314-60-9
			Brominated Compound	0.52	2.35	
				0.05	0.94	40039-93-8
Plating	Metals	6.08E-05	Nickel	0.16	0.61	
			Palladium	0.14	0.53	7440-02-0
			Goild	0.01	0.06	7440-05-3
				0.01	0.02	7440-57-5
Chip	Silicon and inorganic compounds	1.71E-03	Silicon and trace metals	10.48	11.58	7440-21-3
Die Attach	Solder Balls	1.16E-04	Lead	0.73	0.77	
			Tin	0.64	0.68	7439-92-1
			Silver	0.06	0.07	7440-31-5
				0.00	0.00	7440-22-4



Materials Disclosure Disclaimer

The information provided in this Materials Disclosure is, to our knowledge, correct. However, there is no guarantee to completeness or accuracy, as some information is derived from data sources outside the company. Also, there may not be information included in this statement regarding the minute amounts of dopant and metal materials contained within the electrically active or passive devices contained within the finished product.