

Fairchild Semiconductor Product Package Material Disclosure

Package Type	TO-126-3					
Weight of Package (grams)	Maximum	9.09E-01				
	Minimum	8.56E-01				
	Material	Wt in grams	Substance or Material	Wt% in finished product min	Wt% in finished product max	CAS #
Lead Frame	Copper alloy	3.98E-01	Copper	43.77	46.48	
			Iron	43.72	46.27	7440-50-8
			Phosphorus	0.04	0.05	7439-89-6
			Silver (DP)	0.01	0.01	7723-14-0
				0.00	0.15	7440-22-4
Encapsulation	Epoxy	4.68E-01	Silica	51.46	54.64	
			Carbon Black	31.83	42.44	
			Resin	0.00	0.80	
			Antimony Compound	7.96	18.30	1309-64-4
			Brominated Compound	0.27	1.59	
				0.80	2.12	68928-70-1 79-94-7
Plating	Solder	1.53E-02	Tin	0.58	2.88	
			Lead	0.49	2.45	7440-31-5
	or Lead-free Solder	1.53E-02	Tin	0.09	0.43	7439-92-1
Chip	Silicon and inorganic compounds	3.80E-04		0.58	2.88	
				0.58	2.88	7440-31-5
Die Attach	Soft solder	2.30E-04	Lead	0.04	0.05	
			Silicon and trace metals	0.04	0.05	7440-21-3
Wire Bond	Gold wire	2.99E-04	Lead	0.02	0.03	
			Tin	0.02	0.03	7439-92-1
			Silver	< 0.01	< 0.01	7440-31-5
				< 0.01	< 0.01	7440-22-4
			Gold	0.03	0.04	
				0.03	0.04	7440-57-5



Materials Disclosure Disclaimer

The information provided in this Materials Disclosure is, to our knowledge, correct. However, there is no guarantee to completeness or accuracy, as some information is derived from data sources outside the company. Also, there may not be information included in this statement regarding the minute amounts of dopant and metal materials contained within the electrically active or passive devices contained within the finished product.