2N3904 / MMBT3904 / PZT3904
NPN General-Purpose Amplifier

Description
This device is designed as a general-purpose amplifier and switch. The useful dynamic range extends to 100 mA as a switch and to 100 MHz as an amplifier.

Ordering Information

<table>
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<th>Part Number</th>
<th>Marking</th>
<th>Package</th>
<th>Packing Method</th>
<th>Pack Quantity</th>
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<td>2N3904</td>
<td>TO-92 3L</td>
<td>Bulk</td>
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<td>2N3904TA</td>
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<td>2N3904</td>
<td>TO-92 3L</td>
<td>Tape and Reel</td>
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<td>TO-92 3L</td>
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<td>1A</td>
<td>SOT-23 3L</td>
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<td>PZT3904</td>
<td>3904</td>
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Absolute Maximum Ratings\(^{(1), (2)}\)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at \(T_A = 25^\circ C\) unless otherwise noted.

<table>
<thead>
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<th>Symbol</th>
<th>Parameter</th>
<th>Value</th>
<th>Unit</th>
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<tbody>
<tr>
<td>V(_{CEO})</td>
<td>Collector-Emitter Voltage</td>
<td>40</td>
<td>V</td>
</tr>
<tr>
<td>V(_{CBO})</td>
<td>Collector-Base Voltage</td>
<td>60</td>
<td>V</td>
</tr>
<tr>
<td>V(_{EBO})</td>
<td>Emitter-Base Voltage</td>
<td>6.0</td>
<td>V</td>
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<tr>
<td>I(_C)</td>
<td>Collector Current - Continuous</td>
<td>200</td>
<td>mA</td>
</tr>
<tr>
<td>T(<em>J, T</em>{STG})</td>
<td>Operating and Storage Junction Temperature Range</td>
<td>-55 to 150</td>
<td>(^\circ C)</td>
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Notes:
1. These ratings are based on a maximum junction temperature of 150\(^\circ C\).
2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

Thermal Characteristics

Values are at \(T_A = 25^\circ C\) unless otherwise noted.

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<th>Symbol</th>
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<th>Unit</th>
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<td>2N3904</td>
<td>MMBT3904(^{(3)})</td>
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<tr>
<td>P(_D)</td>
<td>Total Device Dissipation</td>
<td>625</td>
<td>350</td>
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<td></td>
<td>Derate Above 25(^\circ C)</td>
<td></td>
<td>5.0</td>
</tr>
<tr>
<td>R(_{\text{(J)JC}})</td>
<td>Thermal Resistance, Junction to Case</td>
<td>83.3</td>
<td></td>
</tr>
<tr>
<td>R(_{\text{(J)JA}})</td>
<td>Thermal Resistance, Junction to Ambient</td>
<td>200</td>
<td>357</td>
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Notes:
3. Device is mounted on FR-4 PCB 1.6 inch X 1.6 inch X 0.06 inch.
4. Device is mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm, mounting pad for the collector lead minimum 6 cm\(^2\).
### Electrical Characteristics

Values are at $T_A = 25^\circ C$ unless otherwise noted.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min.</th>
<th>Max.</th>
<th>Unit</th>
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<td><strong>OFF CHARACTERISTICS</strong></td>
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<tr>
<td>$V_{(BR)CEO}$</td>
<td>Collector-Emitter Breakdown Voltage</td>
<td>$I_C = 1.0$ mA, $I_B = 0$</td>
<td>40</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$V_{(BR)CBO}$</td>
<td>Collector-Base Breakdown Voltage</td>
<td>$I_C = 10$ $\mu$A, $I_E = 0$</td>
<td>60</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$V_{(BR)EBO}$</td>
<td>Emitter-Base Breakdown Voltage</td>
<td>$I_E = 10$ $\mu$A, $I_C = 0$</td>
<td>6.0</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$I_{BL}$</td>
<td>Base Cut-Off Current</td>
<td>$V_{CE} = 30$ V, $V_{EB} = 3$ V</td>
<td>50</td>
<td></td>
<td>nA</td>
</tr>
<tr>
<td>$I_{CEX}$</td>
<td>Collector Cut-Off Current</td>
<td>$V_{CE} = 30$ V, $V_{EB} = 3$ V</td>
<td>50</td>
<td></td>
<td>nA</td>
</tr>
<tr>
<td><strong>ON CHARACTERISTICS</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$h_{FE}$</td>
<td>DC Current Gain</td>
<td>$I_C = 0.1$ mA, $V_{CE} = 1$ V</td>
<td>40</td>
<td></td>
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<tr>
<td></td>
<td></td>
<td>$I_C = 1.0$ mA, $V_{CE} = 1$ V</td>
<td>70</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_C = 10$ mA, $V_{CE} = 1$ V</td>
<td>100</td>
<td>300</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_C = 50$ mA, $V_{CE} = 1$ V</td>
<td>60</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_C = 100$ mA, $V_{CE} = 1$ V</td>
<td>30</td>
<td></td>
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<tr>
<td>$V_{CE(sat)}$</td>
<td>Collector-Emitter Saturation Voltage</td>
<td>$I_C = 10$ mA, $I_B = 1$ mA</td>
<td>0.2</td>
<td></td>
<td>V</td>
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<tr>
<td></td>
<td></td>
<td>$I_C = 50$ mA, $I_B = 5$ mA</td>
<td>0.3</td>
<td></td>
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<td>$V_{BE(sat)}$</td>
<td>Base-Emitter Saturation Voltage</td>
<td>$I_C = 10$ mA, $I_B = 1$ mA</td>
<td>0.65</td>
<td>0.85</td>
<td>V</td>
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<td></td>
<td></td>
<td>$I_C = 50$ mA, $I_B = 5$ mA</td>
<td>0.95</td>
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<td><strong>SMALL SIGNAL CHARACTERISTICS</strong></td>
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<tr>
<td>$f_T$</td>
<td>Current Gain - Bandwidth Product</td>
<td>$I_C = 10$ mA, $V_{CE} = 20$ V</td>
<td>300</td>
<td></td>
<td>MHz</td>
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<tr>
<td></td>
<td></td>
<td>$f = 100$ MHz</td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>$C_{obo}$</td>
<td>Output Capacitance</td>
<td>$V_{CB} = 5$ V, $I_E = 0$, $f = 100$ kHz</td>
<td>4.0</td>
<td></td>
<td>pF</td>
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<tr>
<td>$C_{ib0}$</td>
<td>Input Capacitance</td>
<td>$V_{EB} = 0.5$ V, $I_C = 0$, $f = 100$ kHz</td>
<td>8.0</td>
<td></td>
<td>pF</td>
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<tr>
<td>$NF$</td>
<td>Noise Figure</td>
<td>$I_C = 100$ $\mu$A, $V_{CE} = 5$ V, $R_S = 1.0$ k$\Omega$, $f = 10$ Hz to 15.7 kHz</td>
<td>5.0</td>
<td></td>
<td>dB</td>
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<td><strong>SWITCHING CHARACTERISTICS</strong></td>
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<tr>
<td>$t_d$</td>
<td>Delay Time</td>
<td>$V_{CC} = 3.0$ V, $V_{BE} = 0.5$ V</td>
<td>35</td>
<td></td>
<td>ns</td>
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<tr>
<td>$t_r$</td>
<td>Rise Time</td>
<td>$I_C = 10$ mA, $I_{B1} = 1$ mA</td>
<td>35</td>
<td></td>
<td>ns</td>
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<tr>
<td>$t_s$</td>
<td>Storage Time</td>
<td>$V_{CC} = 3.0$ V, $I_C = 10$ mA, $I_{B1} = I_{B2} = 1$ mA</td>
<td>200</td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td>$t_f$</td>
<td>Fall Time</td>
<td>$I_{B1} = I_{B2} = 1$ mA</td>
<td>50</td>
<td></td>
<td>ns</td>
</tr>
</tbody>
</table>

**Note:**

5. Pulse test: pulse width ≤ 300 $\mu$s, duty cycle ≤ 2.0%.
Typical Performance Characteristics

Figure 1. Typical Pulsed Current Gain vs. Collector Current

Figure 2. Collector-Emitter Saturation Voltage vs. Collector Current

Figure 3. Base-Emitter Saturation Voltage vs. Collector Current

Figure 4. Base-Emitter On Voltage vs. Collector Current

Figure 5. Collector Cut-Off Current vs. Ambient Temperature

Figure 6. Capacitance vs. Reverse Bias Voltage
Typical Performance Characteristics (Continued)

Figure 7. Noise Figure vs. Frequency

Figure 8. Noise Figure vs. Source Resistance

Figure 9. Current Gain and Phase Angle vs. Frequency

Figure 10. Power Dissipation vs. Ambient Temperature

Figure 11. Turn-On Time vs. Collector Current

Figure 12. Rise Time vs. Collector Current
Typical Performance Characteristics (Continued)

Figure 13. Storage Time vs. Collector Current

![Storage Time vs. Collector Current](image1)

Figure 14. Fall Time vs. Collector Current

![Fall Time vs. Collector Current](image2)

Figure 15. Current Gain

![Current Gain](image3)

Figure 16. Output Admittance

![Output Admittance](image4)

Figure 17. Input Impedance

![Input Impedance](image5)

Figure 18. Voltage Feedback Ratio

![Voltage Feedback Ratio](image6)
Test Circuits

Figure 19. Delay and Rise Time Equivalent Test Circuit

Figure 20. Storage and Fall Time Equivalent Test Circuit
NOTES: UNLESS OTHERWISE SPECIFIED
A. DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC.
B. ALL DIMENSIONS ARE IN MILLIMETERS.
C. DRAWING CONFORMS TO ASME Y14.5M-2009.
D. DRAWING FILENAME: MKT-2A03FREV3.
E. FAIRCHILD SEMICONDUCTOR.
NOTES: UNLESS OTHERWISE SPECIFIED

A) DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.
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D) DRAWING FILENAME: MKT-ZA03DREV4.
NOTES: UNLESS OTHERWISE SPECIFIED

A) DRAWING BASED ON JEDEC REGISTRATION TO-261, VARIATION AA.
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C) ALL DIMENSIONS ARE IN MILLIMETERS.
D) DRAWING CONFORMS TO ASME Y14.5M-1994.
E) LANDPATTERN NAME: SOT230P700X180-48N
F) DRAWING FILENAME: MKT-MA04AREV2
NOTES: UNLESS OTHERWISE SPECIFIED

A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H.
B) ALL DIMENSIONS ARE IN MILLIMETERS.
C) DIMENSIONS ARE INCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS.
D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M - 2009.
E) DRAWING FILE NAME: MA03DREV11
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PRODUCT STATUS DEFINITIONS

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<th>Datasheet Identification</th>
<th>Product Status</th>
<th>Definition</th>
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<td>Advance Information</td>
<td>Formative / In Design</td>
<td>Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.</td>
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<tr>
<td>Preliminary</td>
<td>First Production</td>
<td>Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.</td>
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<tr>
<td>No Identification Needed</td>
<td>Full Production</td>
<td>Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.</td>
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<tr>
<td>Obsolete</td>
<td>Not In Production</td>
<td>Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.</td>
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