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FAIRCHILD SEMICONDUCTOR®

November 2013

FQP30N06L

N-Channel QFET® MOSFET

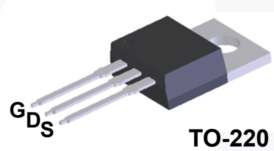
60 V, 32 A, 35 mΩ

Description

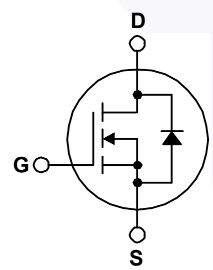
This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 32 A, 60 V, $R_{DS(on)}$ = 35 mΩ (Max.) @ V_{GS} = 10 V, I_D = 16 A
- Low Gate Charge (Typ. 15 nC)
- Low Crss (Typ. 50 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating



TO-220



Absolute Maximum Ratings
T_C = 25°C unless otherwise noted.

Symbol	Parameter			FQP30N06L	Unit
V_{DSS}	Drain-Source Voltage			60	V
I_D	Drain Current	- Continuous (T_C = 25°C)		32	A
		- Continuous (T_C = 100°C)		22.6	A
I_{DM}	Drain Current	- Pulsed	(Note 1)	128	A
V_{GSS}	Gate-Source Voltage			± 20	V
E_{AS}	Single Pulsed Avalanche Energy		(Note 2)	350	mJ
I_{AR}	Avalanche Current		(Note 1)	32	A
E_{AR}	Repetitive Avalanche Energy		(Note 1)	7.9	mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	7.0	V/ns
P_D	Power Dissipation (T_C = 25°C)			79	W
		- Derate above 25°C		0.53	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range			-55 to +175	°C
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds			300	°C

Thermal Characteristics

Symbol	Parameter	FQP30N06L	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.90	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQP30N06L	FQP30N06L	TO-220	Tube	N/A	N/A	50 units

Electrical Characteristics

T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0 V, I_D = 250 µA	60	--	--	V
ΔBV_{DSS} / ΔT_J	Breakdown Voltage Temperature Coefficient	I_D = 250 µA, Referenced to 25°C	--	0.06	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 60 V, V_{GS} = 0 V	--	--	1	µA
		V_{DS} = 48 V, T_C = 150°C	--	--	10	µA
I_{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 20 V, V_{DS} = 0 V	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	V_{GS} = -20 V, V_{DS} = 0 V	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250 µA	1.0	--	2.5	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	V_{GS} = 10 V, I_D = 16 A V_{GS} = 5 V, I_D = 16 A	-- --	0.027 0.035	0.035 0.045	Ω
g_{FS}	Forward Transconductance	V_{DS} = 25 V, I_D = 16 A	--	24	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1.0 MHz	--	800	1040	pF
C_{oss}	Output Capacitance		--	270	350	pF
C_{rss}	Reverse Transfer Capacitance		--	50	65	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 30 V, I_D = 16 A, R_G = 25 Ω (Note 4)	--	15	40	ns
t_r	Turn-On Rise Time		--	210	430	ns
$t_{d(off)}$	Turn-Off Delay Time		--	60	130	ns
t_f	Turn-Off Fall Time		--	110	230	ns
Q_g	Total Gate Charge	V_{DS} = 48 V, I_D = 32 A, V_{GS} = 5 V (Note 4)	--	15	20	nC
Q_{gs}	Gate-Source Charge		--	3.5	--	nC
Q_{gd}	Gate-Drain Charge		--	8.5	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	32	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	128	A
V_{SD}	Drain-Source Diode Forward Voltage	V_{GS} = 0 V, I_S = 32 A	--	--	1.5	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0 V, I_S = 32 A, dI_F / dt = 100 A/µs	--	60	--	ns
Q_{rr}	Reverse Recovery Charge		--	90	--	nC

Notes:
1. Repetitive Rating : Pulse width limited by maximum junction temperature.
2. L = 400 µH, I_{AS} = 32 A, V_{DD} = 25 V, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 32 A, di/dt ≤ 300 A/us, V_{DD} ≤ $BV_{DSS,}$ starting T_J = 25°C.
4.Essentially independent of operating temperature.

Typical Characteristics

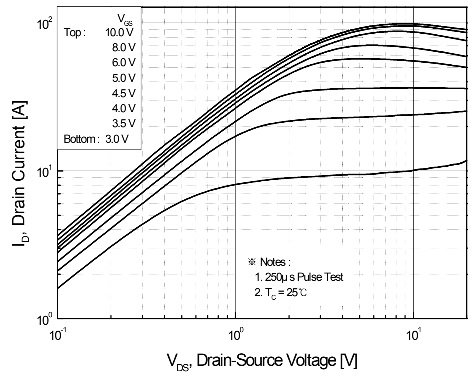


Figure 1. On-Region Characteristics

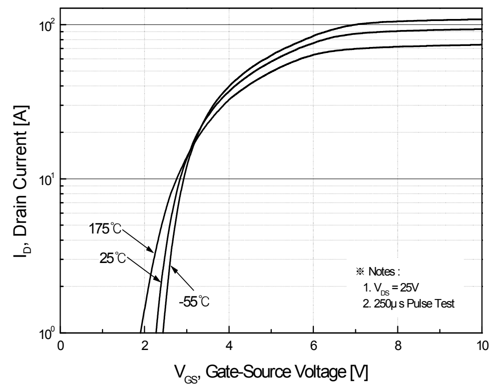


Figure 2. Transfer Characteristics

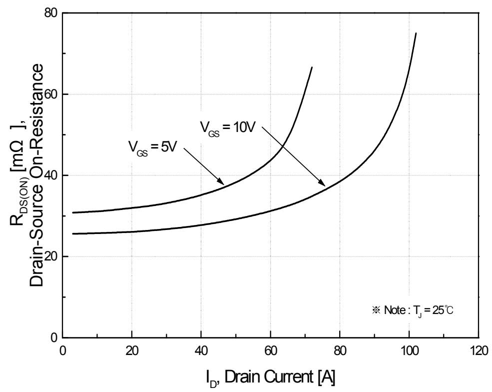


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

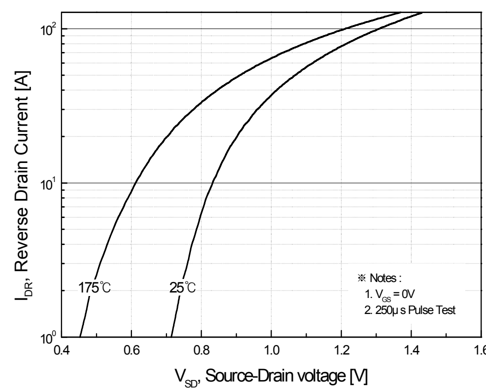


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

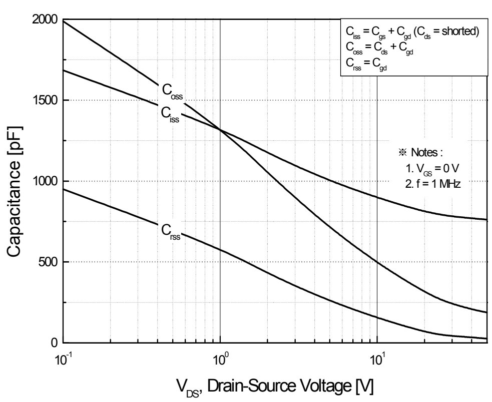


Figure 5. Capacitance Characteristics

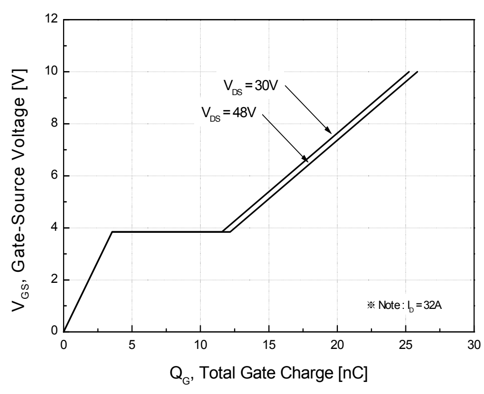


Figure 6. Gate Charge Characteristics

Typical Characteristics (continued)

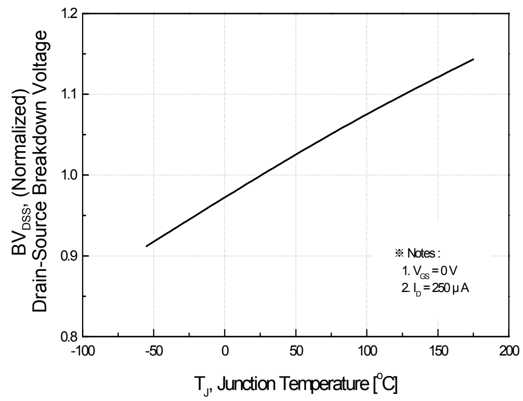


Figure 7. Breakdown Voltage Variation vs. Temperature

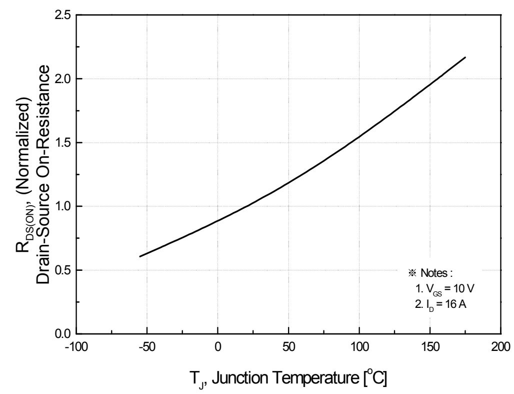


Figure 8. On-Resistance Variation vs. Temperature

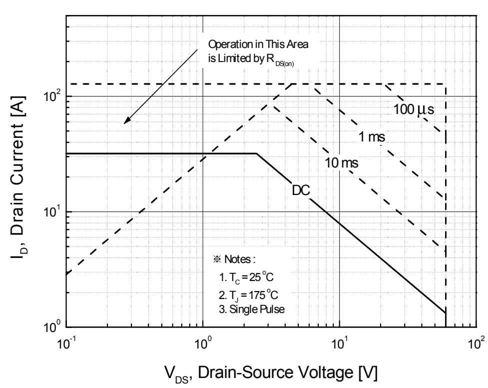


Figure 9. Maximum Safe Operating Area

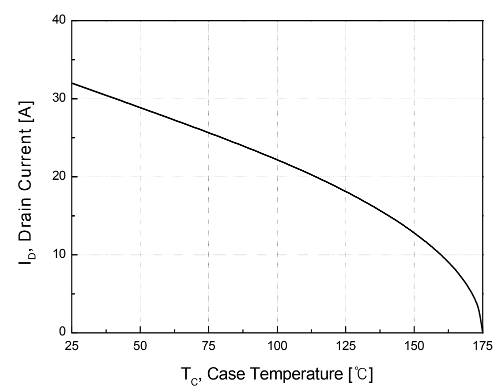


Figure 10. Maximum Drain Current vs. Case Temperature

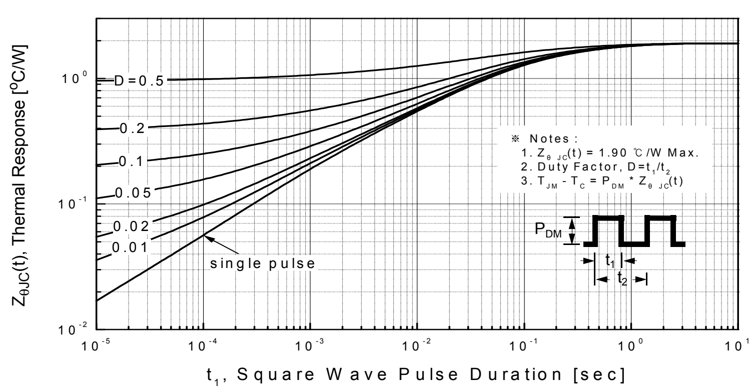


Figure 11. Transient Thermal Response Curve

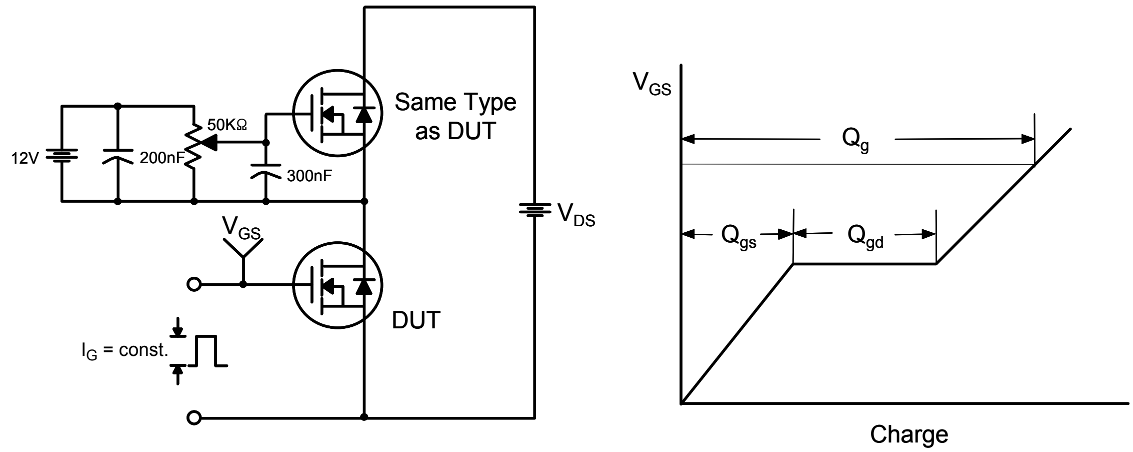


Figure 12. Gate Charge Test Circuit & Waveform

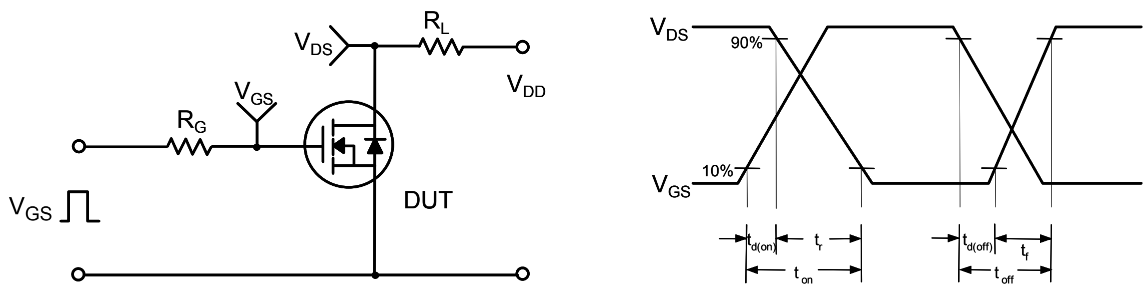


Figure 13. Resistive Switching Test Circuit & Waveforms

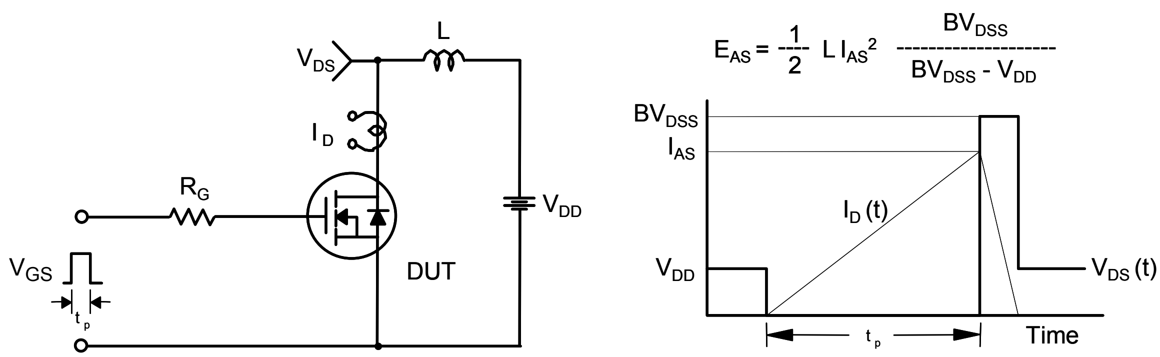
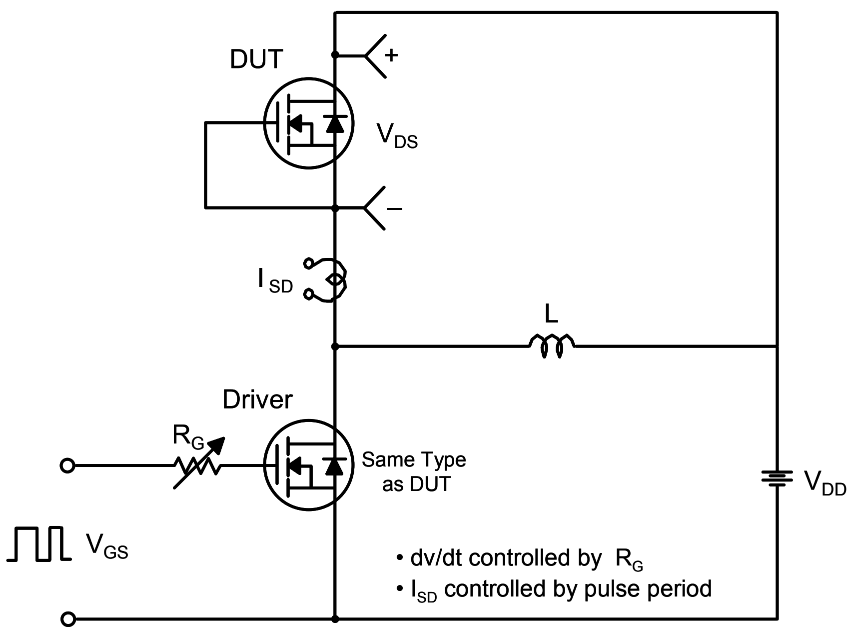


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



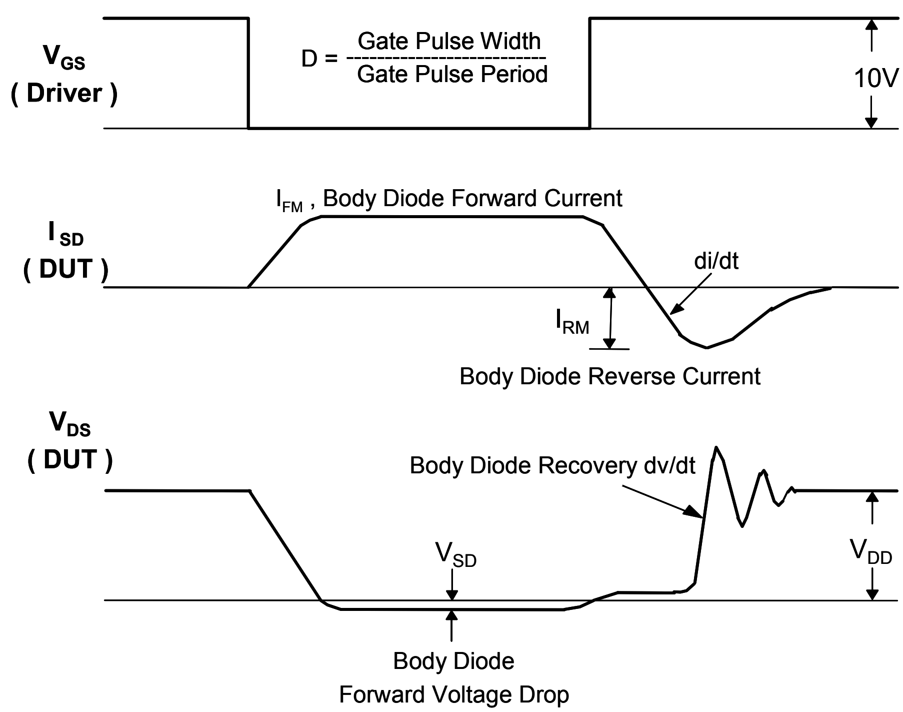


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

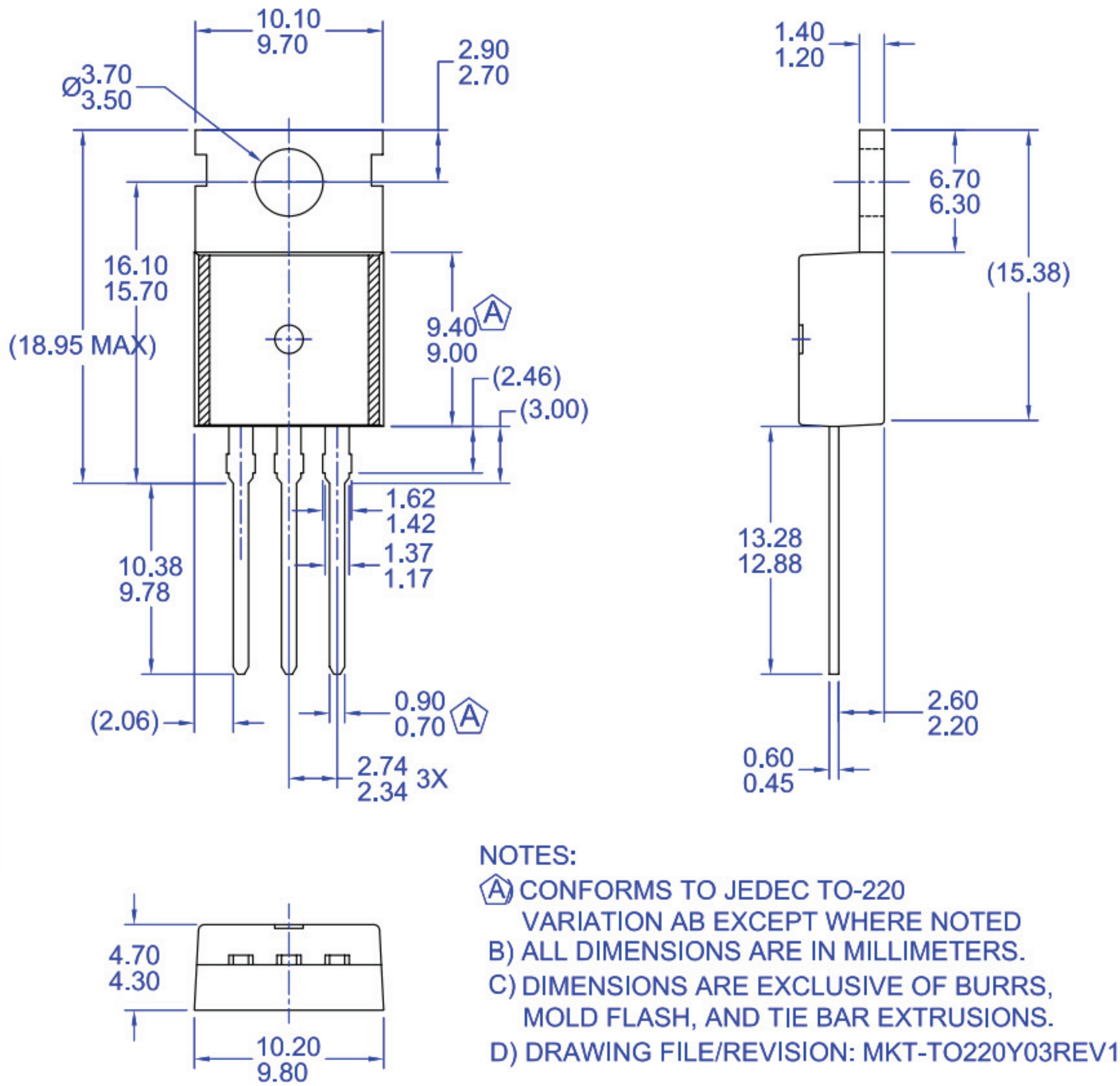


Figure 16. TO220, Molded, 3-Lead, Jedec Variation AB

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